

# Method for correcting luminance and chrominance defects of a matrix display and matrix display and circuit for carrying out the method

Patent number: EP0757498  
Publication date: 1997-02-05  
Inventor: WATRIN THIERRY [FR]  
Applicant: THOMSON MULTIMEDIA SA [FR]  
Classification:  
- international: H04N9/31  
- european: H04N9/31V; H04N9/73  
Application number: EP19960401672 19960725  
Priority number(s): FR19950009424 19950802

**Also published as:**

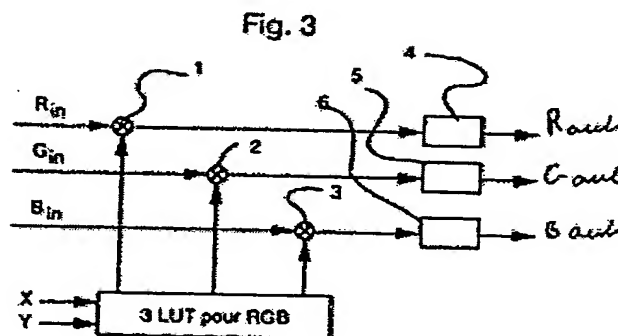
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**Abstract of EP0757498**

The method involves using a circuit containing three memories for primary colour correction look-up tables (LUT) and multipliers for corresponding inputs from a colour decoder. Correction is performed on a pixel to pixel basis. The multiplication products are processed by individual peak limiters (4-6). Unitary correction is applied to the slightly altered part of the picture and multiplied by a coefficient corresponding to the average of the look-up table contents over a number of preceding pixels. Smoothing can be applied to preserve detail threatened by chromatic overcorrection.



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